



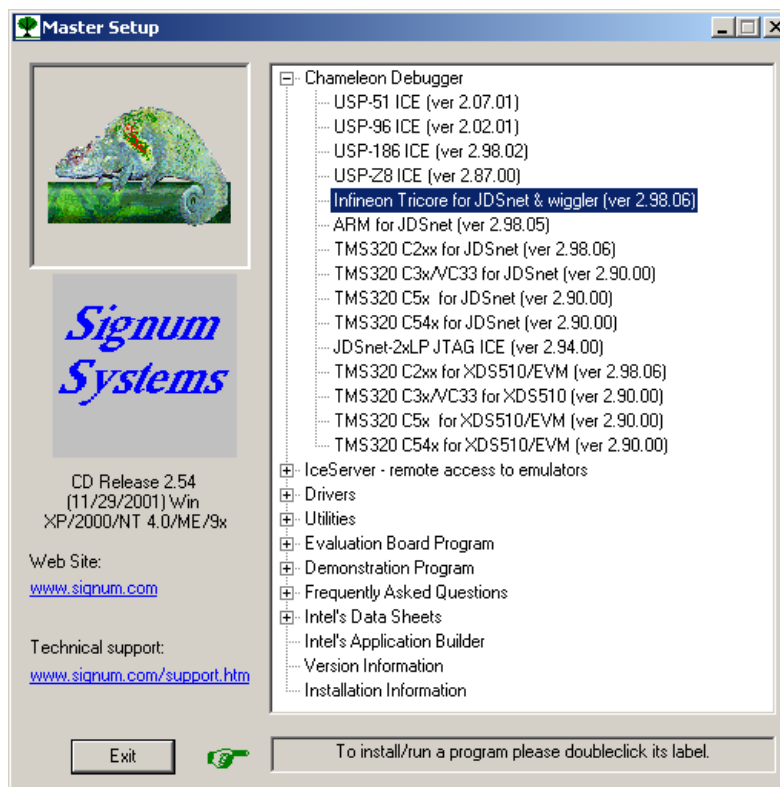
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# Chameleon with Infineon's TriCore Evaluation Board (direct parallel port cable – without JDSnet) Installation Instructions

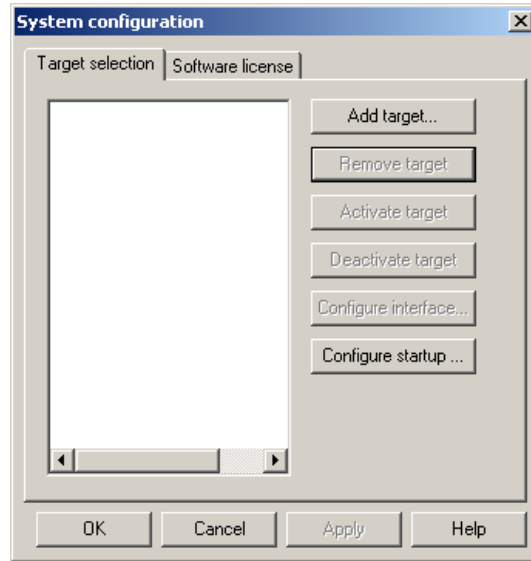
SWII-ChamTriCoreEVB 11.29.01.14.14

**PURPOSE:** This document describes the Chameleon debugger installation process for TriCore Evaluation Board from Infineon Technologies using a direct parallel port cable. Installation of Chameleon for the JDSnet emulator is described in a separate document.

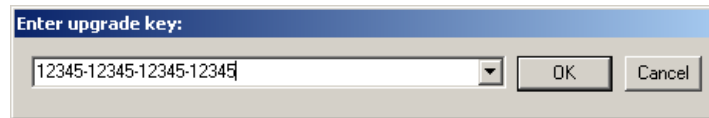
1. Insert the *Development Tools for Windows9x/NT and 2000* CD into your CDROM drive. Select **Chameleon Debugger** and double-click **Infineon Tricore for JDSnet & wiggler** in the CD-ROM Master Setup window:



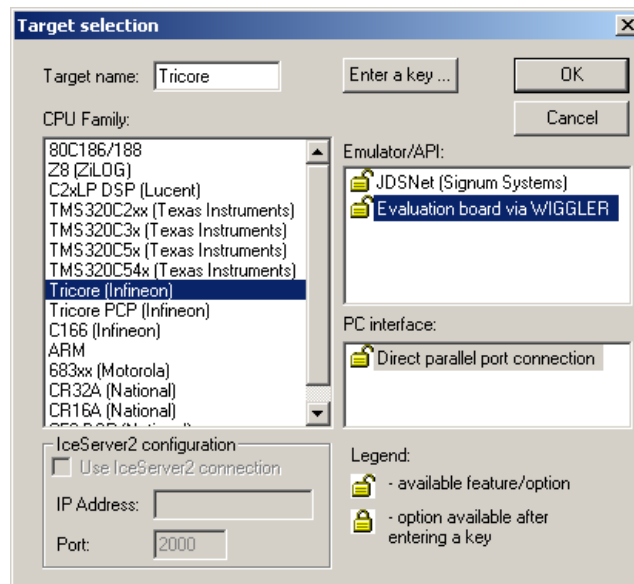
2. Complete the installation process, start Chameleon and click the **Add target ...** button in the **System Configuration** window:



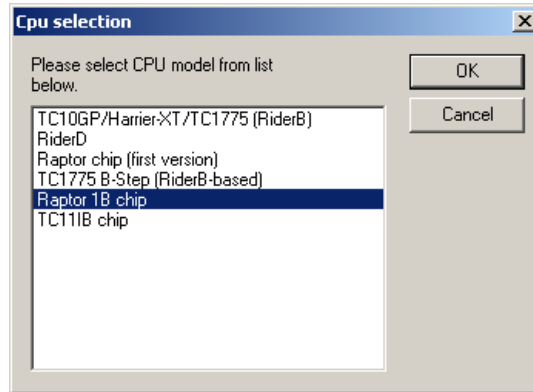
- Click the **Add target ...** button to display the **Target Selection** window (see below). Click the **Enter a key ...** button and enter the license key from the Product User License Certificate:



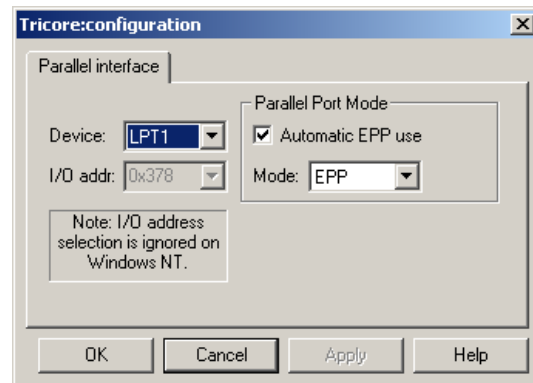
- In the **Target selection** dialog box, select **Tricore (Infineon)** as the CPU Family and **Evaluation board via WIGGLER** as the Emulator/API:



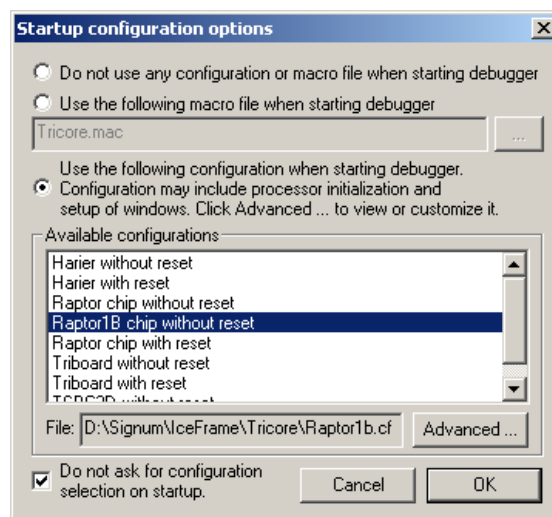
- If prompted, specify the CPU model you are using.



7. You will be asked to specify the LPT communication port in the following dialog box. **Parallel Port Mode** settings will be ignored, because direct parallel port connection uses own protocol.

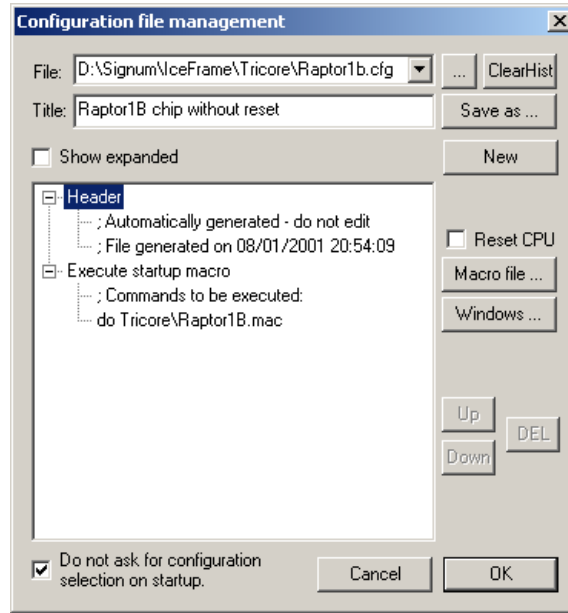


8. Chameleon provides a flexible startup configuration setup. In the **Startup configuration options** dialog box, select the startup configuration that fits your needs:



If you choose to use a startup macro file, you may select one by typing its name, or by clicking the “...” button and browsing your file system.

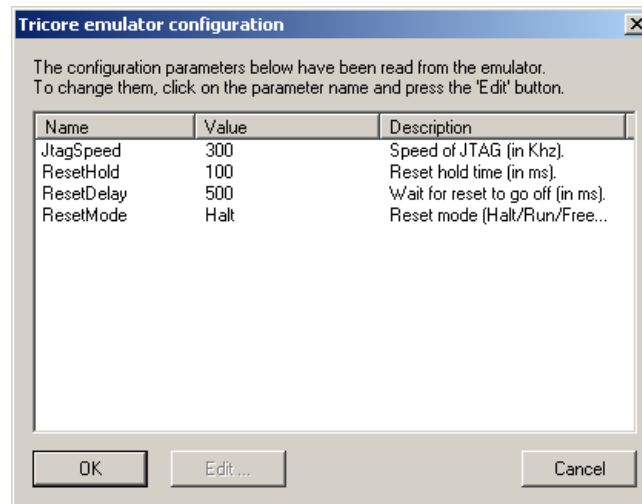
In general, configuration files offer more control over debugger initialization than startup macro files. A list of available configurations is obtained from one of the .cfg files found in the Tricore folder. Click the **Advanced** button to view or customize that file.



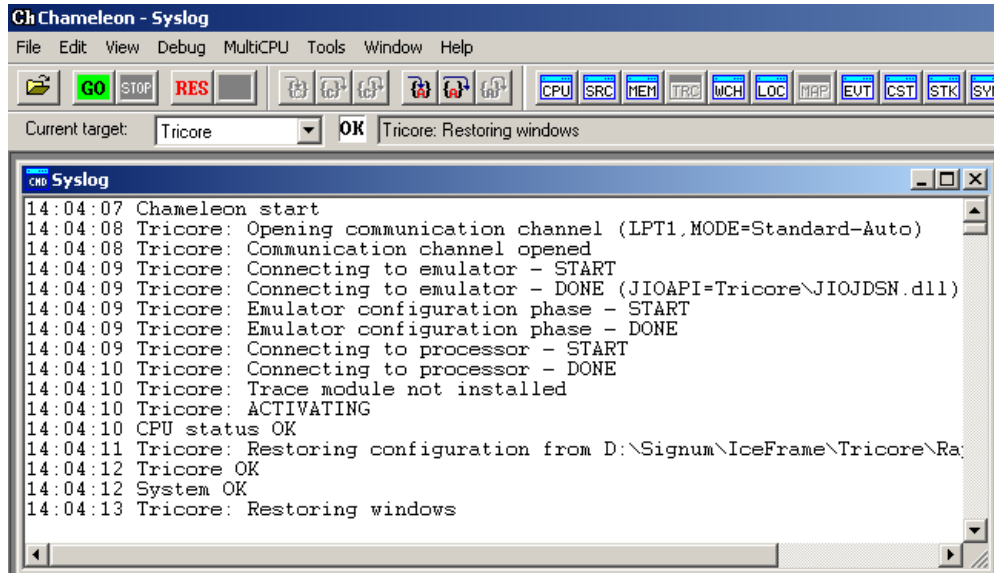
To create your own .cfg files based on the files distributed with Chameleon, use the **Save as** button rather than overwrite the originals that may be needed for further reference.

Additionally, the **Windows** button allows you to specify which Chameleon windows are to be opened when the debugger starts.

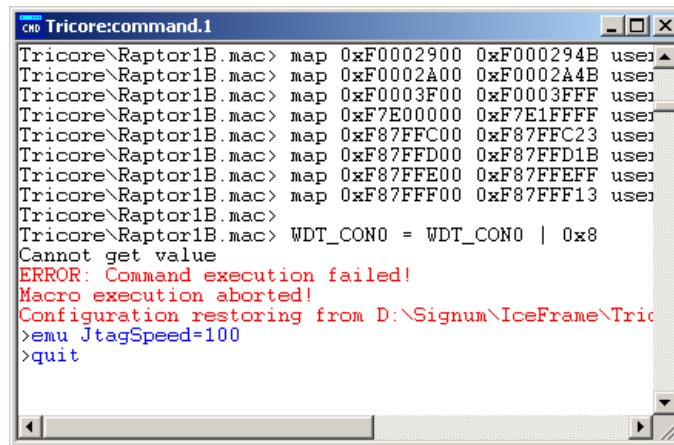
- The parameters in the box control the JTAG clock and reset mode behavior. If you experience connection stability problems please double click on **JtagSpeed** parameter name and make JTAG clock slower.



10. Make sure that the TriCore evaluation board is powered and connected to an LPT communication port. Click **OK**. Chameleon debugger will connect to the board. The debugger's initial screen should look like this:



11. Due the wild variety of PC parallel port designs in existence, in general, it is impossible to provide a fast and reliable JTAG emulation connection over the parallel port. If you experience problems, such as the lack of connection between Chameleon and the processor or difficulties with reading/writing to some registers/memories, try to adjust the **JtagSpeed** emulation parameter and restart the debugger. It can be done during target creation (see point 9. above) or by issuing command **emu JtagSpeed=value** in command window:



The command **quit** closes debugger session. Specified JTAG speed will be used in next Chameleon session.

Please be aware that some of the advanced features, such as faster download speed, reset/power-up handling, are not available when using a direct parallel port cable. The JDSnet emulator offers much faster (10-20 times), more reliable and robust emulation of TriCore devices. As the unit relies on a JTAG connection, no direct cable circuitry on the target board is necessary. Additionally, the JDSnet OCDS L2 trace option makes code debugging much easier and more efficient.

